

Supports the following
ARM® Processor Cores:

ARM7, ARM9, ARM9E,
ARM11, XScale, OMAP,
Cortex-M1, Cortex-M3,
Cortex-R4 and
Cortex-A8

Arium LC-500 JTAG Emulator



The LC-500 is Arium's most popular JTAG emulator for targets with ARM® cores. The unit offers reliable run control and intuitive stepping features. It's designed for speed, too, with fast file and image downloads and quick stepping through code. When coupled with

SourcePoint™, Arium's flagship debugging software, the LC-500 operates on Microsoft® Windows® and Linux hosts and offers debug of Linux kernel, device driver, and application source code. Featuring real-time event management and independent processor control, the LC-500 delivers superb visibility to and manipulation of source code.

Key Benefits

- **Powerful** - The LC-500 offers unparalleled run control functionality and execution processing for ARM-architecture processors.
- **Integrated & Intuitive** - The LC-500, with its companion software debugger, offers a highly integrated, intuitive environment designed to help you minimize iterative steps and shorten your debug cycle.
- **User-friendly** - The LC-500 is easy to install and configure, giving you a fast start to your debug project.
- **Flexible** - The LC-500 works with a number of ARM-architecture cores/processors. No additional tools are required when moving from one architecture to another.
- **Value Packed** - Full ARM and Thumb™ instruction set debug at a competitive price.
- **Fully Supported** - All Arium units produced for the embedded market are fully supported by our highly trained staff of applications engineers.

ARM JTAG debug support:

- Non-intrusive debug
- Real-time operation
- JTAG rate up to 20 MHz
- Reliable run control
- Superior breakpoint control
- Semihosting support
- CoreSight™ support
- RealMonitor™ support
- PLD programming
- Flash programming
- Linux OS-aware kernel, device driver, and application debug

Connection types:

- USB 1.1
- 10Base-T/100Base-T Ethernet

Supported toolchains:

- ARM Developer Suite 1.2
- ARM RealView® Developers Suite
- Green Hills® C/C++
- GNU C/C++
- IAR Embedded Workbench®

Supported processors:

- **ARM RISC core architecture**
 - ARM7™
 - ARM9™
 - ARM11™
 - Cortex™

XScale architecture

- Application processors
- I/O processors
- Control plane processors
- Network processors

Texas Instruments processors

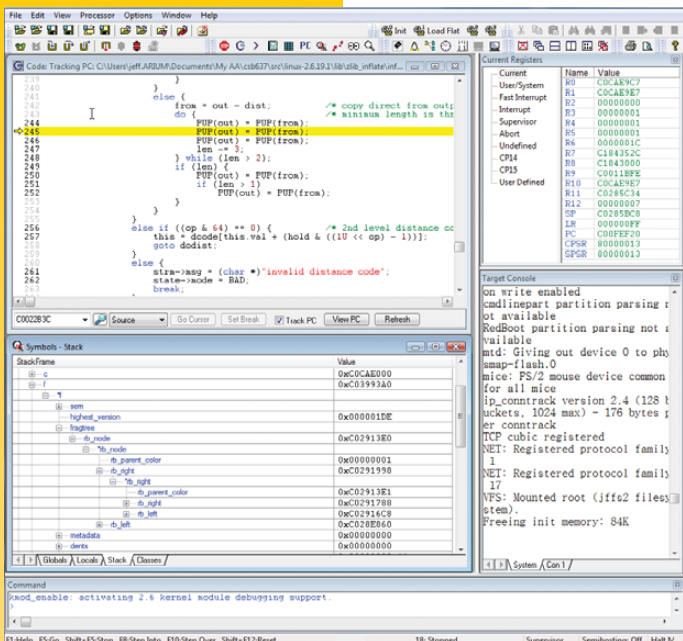
- OMAP™



The LC-500 uses an ARM-powered® processor for robust performance and throughput.



Arium LC-500 JTAG Emulator



SourcePoint™ Interface

Included with the LC-500 is Arium's SourcePoint debugging software. The debugger interface is part of the company's core technology, developed specifically for SoC design and debug. The 32-bit application runs on Microsoft® Windows® 2000/XP/Vista™ and numerous Linux hosts and supports targets running various operating systems, including Linux.

Key features include:

- Full ARM and Thumb™ support
- Innovative, seamless approach to Linux kernel, device driver, and applications debug
- Single view of all I/O-mapped devices
- Multi-core and heterogeneous-core debug
- Independent processor control via the software
- Code profiling
- Robust, user-friendly command language

The Company

Arium has been a major supplier of emulator debugging tools for more than 20 years, servicing both the embedded and PC markets. Arium hardware-assisted solutions support ARM®-based processor cores, XScale, and TI OMAP™ processors, as well as AMD and Intel® laptop, desktop, and server processors.

Arium's mission is to meet and exceed the needs of firmware and software developers by providing them with the right tools to help them minimize their debug cycle.

It's Hard to Compete Without the Right Tools

Specifications

Environmental

32-90° F (0-31° C)
Maximum humidity - 85%

Communications

10/100Base-T Ethernet
USB 1.1

JTAG Frequency

Up to 20 MHz

Download Speed

>200 KBytes/sec

Trig In

>50 Ohm, 155 ns maximum delay

Trig Out

50 Ohm, 125 ns maximum delay

Dimensions (inches)

(W) 3.800
(L) 5.004
(H) 1.510

JTAG

20-pin debug port connector

PC Host Requirements

- PC host (Intel or AMD processor)
- Microsoft Windows 2000/XP/Vista or Linux (Validated against the following distributions: Red Hat and Red Hat Fedora Core or Enterprise, Mandriva, and Novell SUSE. Contact Arium for latest versions.)
- SVGA monitor (1024 x 768 or higher)
- 60 MB hard disk space, 128 MB RAM
- 10/100Base-T Ethernet or USB 1.1

Ordering Information

Standard

LC-500	Base unit
SourcePoint	SourcePoint for Windows
Warranty	90-day service agreement

Options:

SP-Linux	SourcePoint for Linux hosts
DPA-2014	20-14 pin JTAG adapter
STAR1-LC	Extended service agreement

